

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Group Art Unit: To Be Assigned

Examiner: To Be Assigned

In Re PATENT APPLICATION Of:

Applicants :	Masaru TAKAISHI)	INFORMATION DISCLOSURE STATEMENT
Serial No. :	To Be Assigned)	
Filed :	March 26, 2004)	
For :	METHOD FOR MANUFACTURING SEMICONDUCTOR DEVICE AND SEMICONDUCTOR DEVICE)	
Attorney Ref. :	AI 332)	

March 26, 2004

Commissioner for Patents
P. O. Box 1450
Alexandria VA 22313-1450

Sir:

This is an information disclosure statement submitted in compliance with the timing requirements of 37 C.F.R. §1.97(b)(1).

Two U.S. Patents are listed on the attached Form PTO-1449, which are discussed on page 1 of the application.

Since this Information Disclosure Statement is being filed with the application, no certification or fee is required, and the requirements of 37 C.F.R. §§1.97 and 1.98 are deemed to be fully met as to the documents listed. Consideration of the listed documents is respectfully requested.

Respectfully submitted,



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March 26, 2004

Date

RHB:tl

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FORM PTO-1449 INFORMATION DISCLOSURE STATEMENT		Atty Docket		Application No.			
		AI 332		To Be Assigned			
		Applicant					
		Masaru TAKAISHI					
Filing Date		Group Unit					
March 26, 2004		To Be Assigned					
U.S. PATENT DOCUMENTS							
Examiner Initial		Document Number	Date	Name	Class	Sub-Class	Filing Date
	AA	4,767,722	08/30/88	Blanchard			
	AB	5,034,785	07/23/91	Blanchard			
	AC						
	AD						
	AE						
	AF						
	AG						
	AH						
	AI						
	AJ						
FOREIGN PATENT DOCUMENTS							
		Document Number	Date	Country	Class	Sub-Class	Trans-lation
	AK						
	AL						
	AM						
	AN						
	AO						
OTHER (Including Author, Title, Date, Pertinent Pages, etc.)							
	AP						
	AQ						
	AR						
	AS						
Examiner				Date Considered			
EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609; draw line through citation if not in conformance and not considered. Include copy of this form with next communication to Applicant.							